

Fig. 1

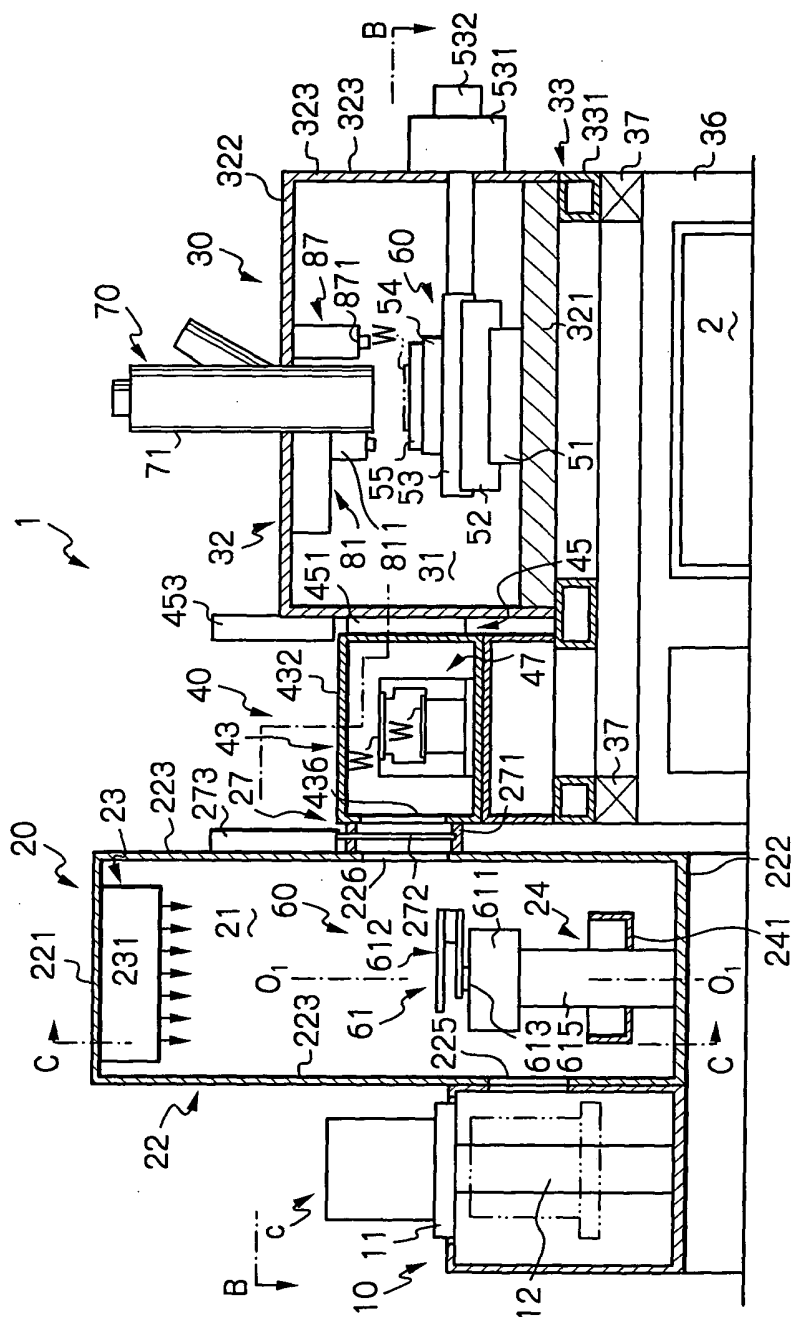


Fig. 2 A

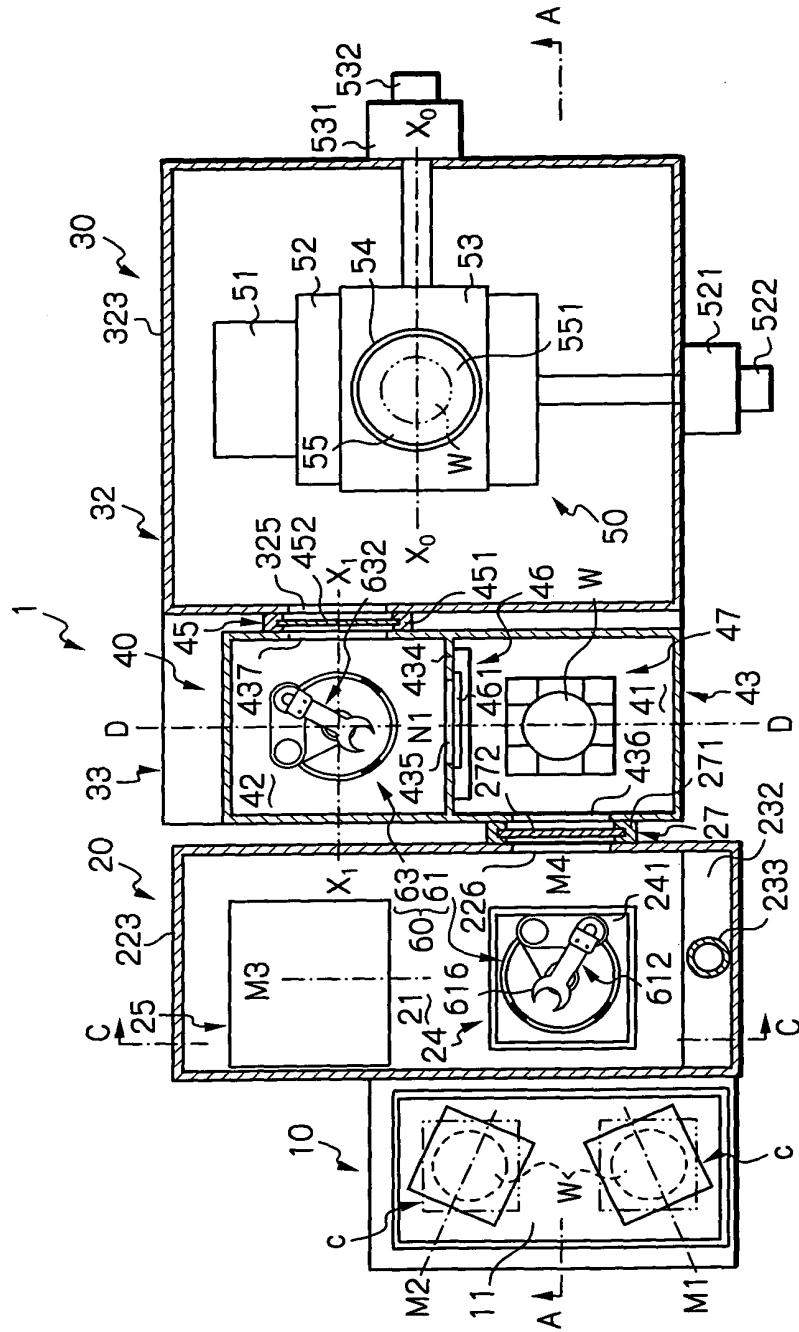


Fig. 2 B

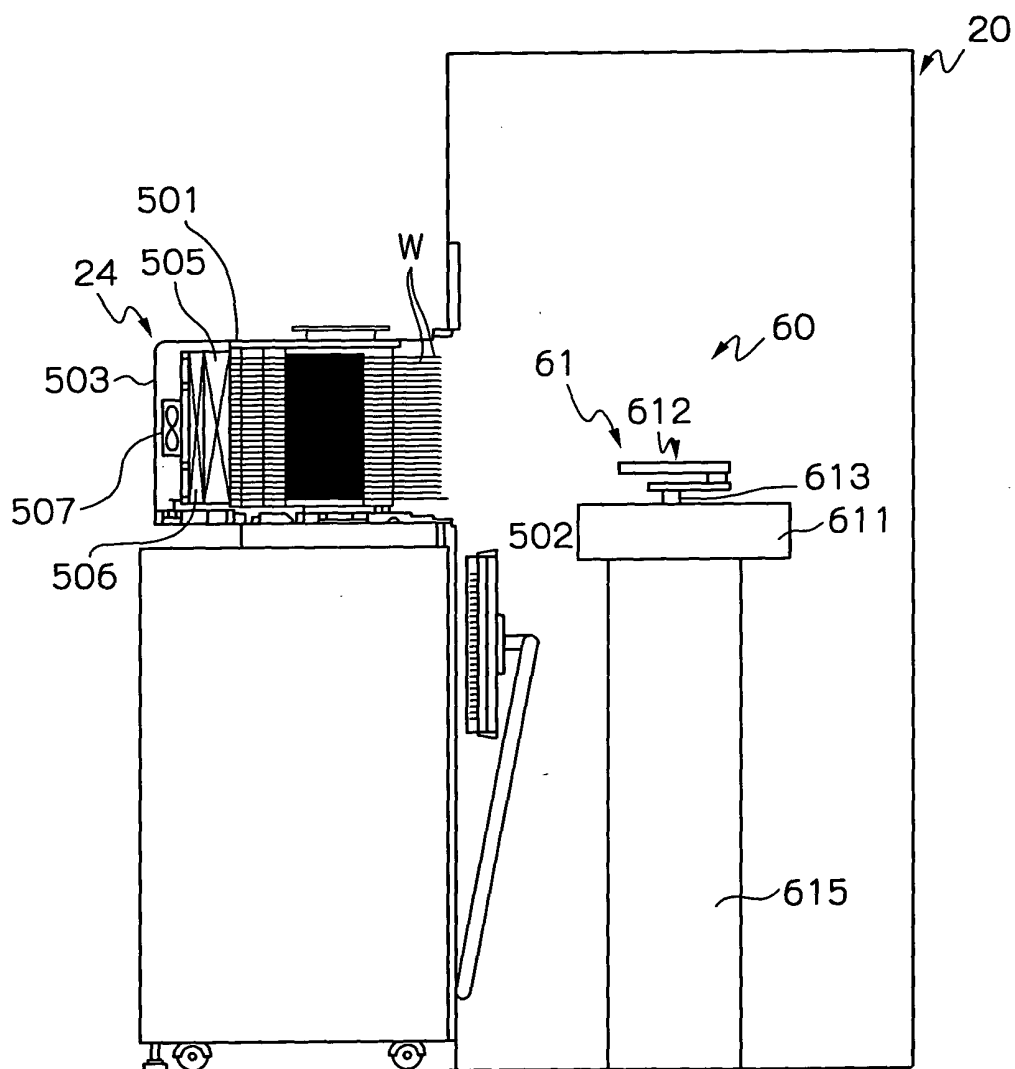


Fig. 3

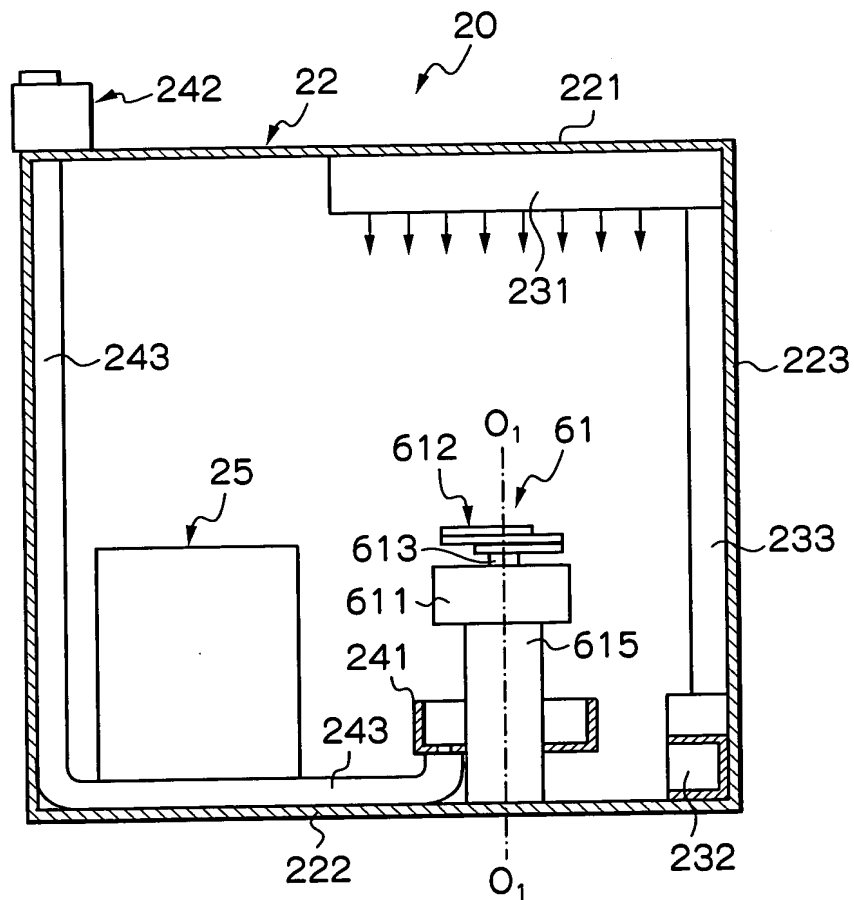




Fig. 4

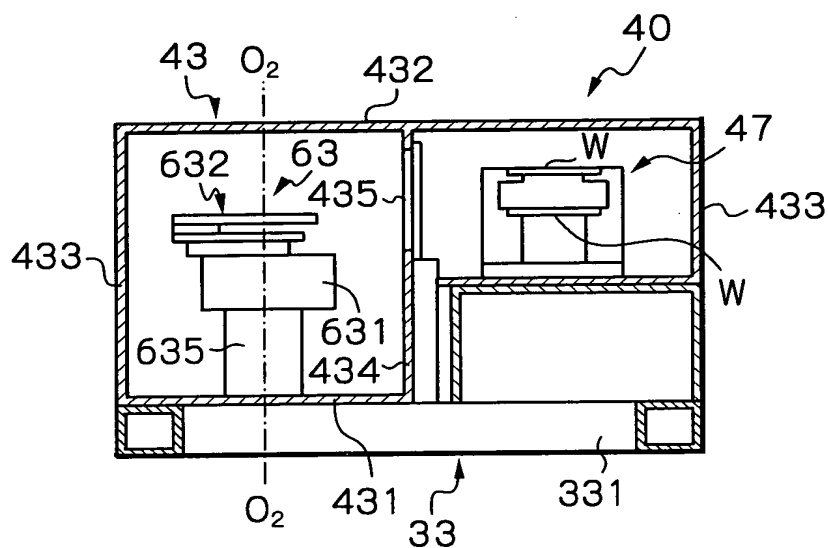


Fig. 5 A

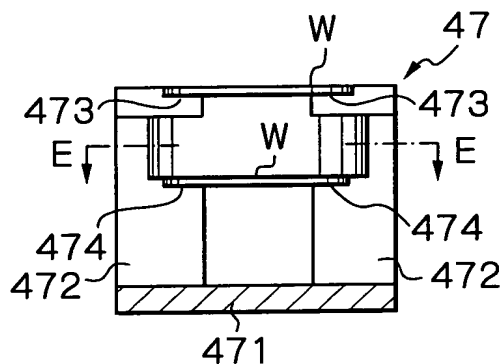
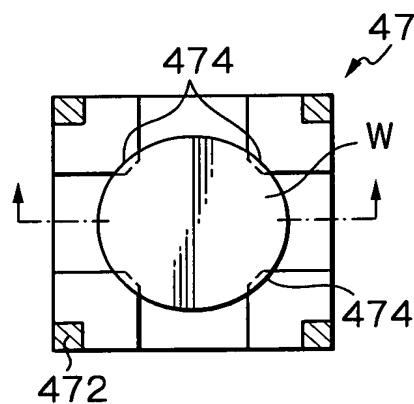


Fig. 5 B



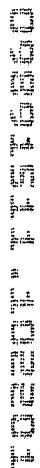


Fig. 7

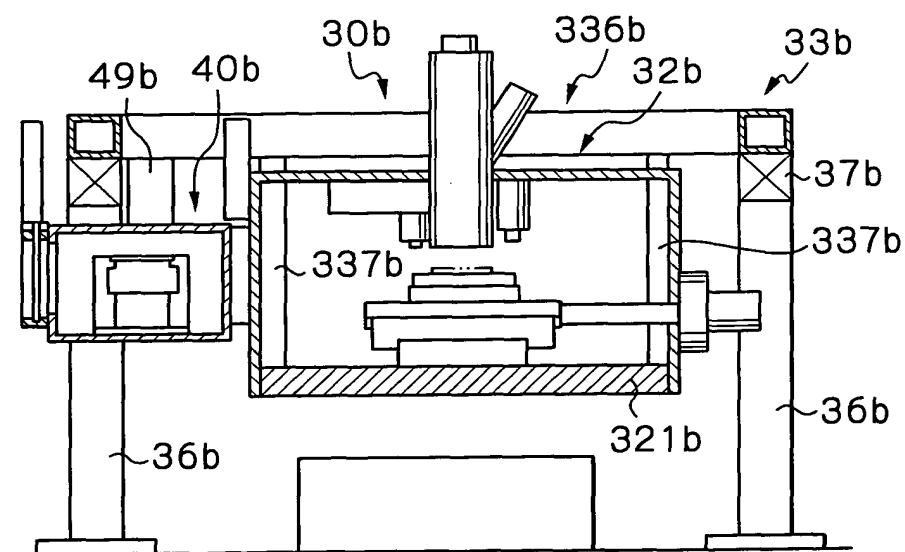
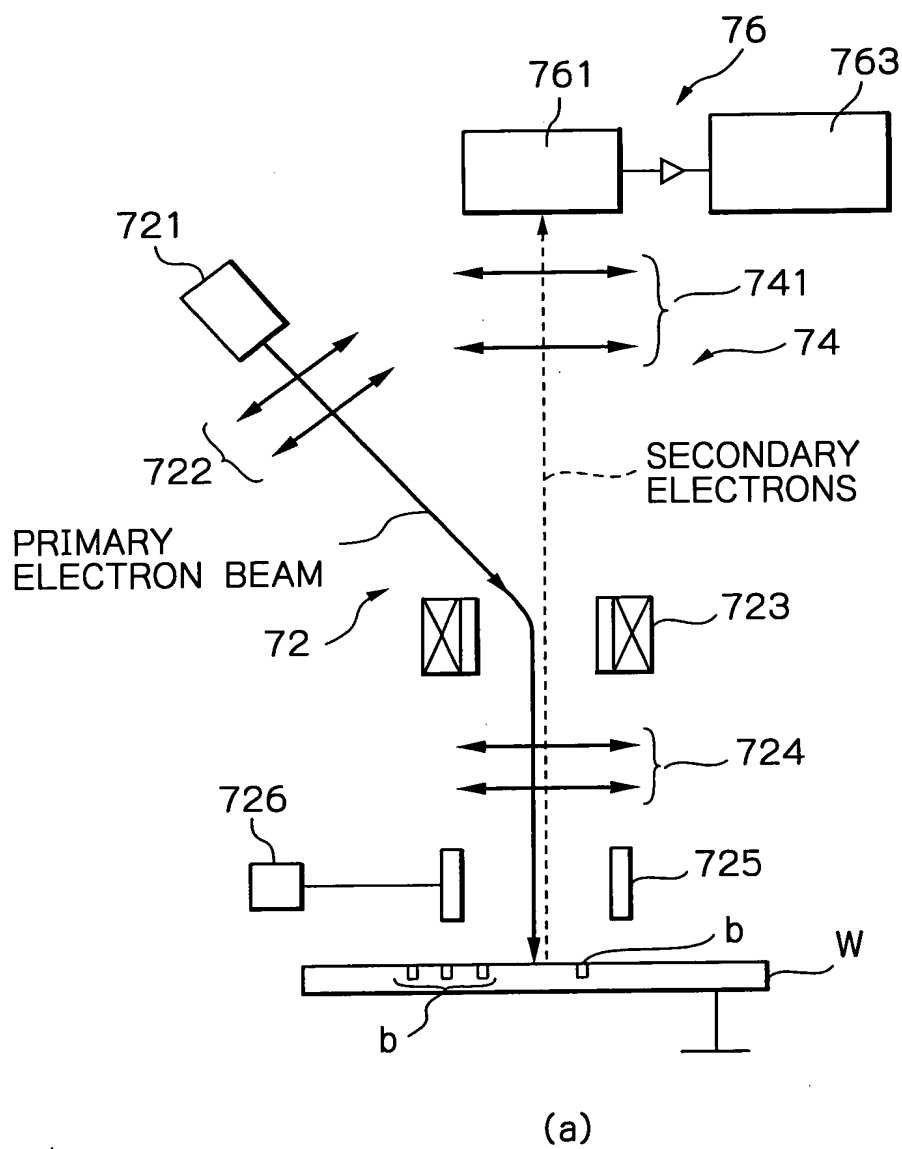




Fig. 8



099454 402204

Fig. 9

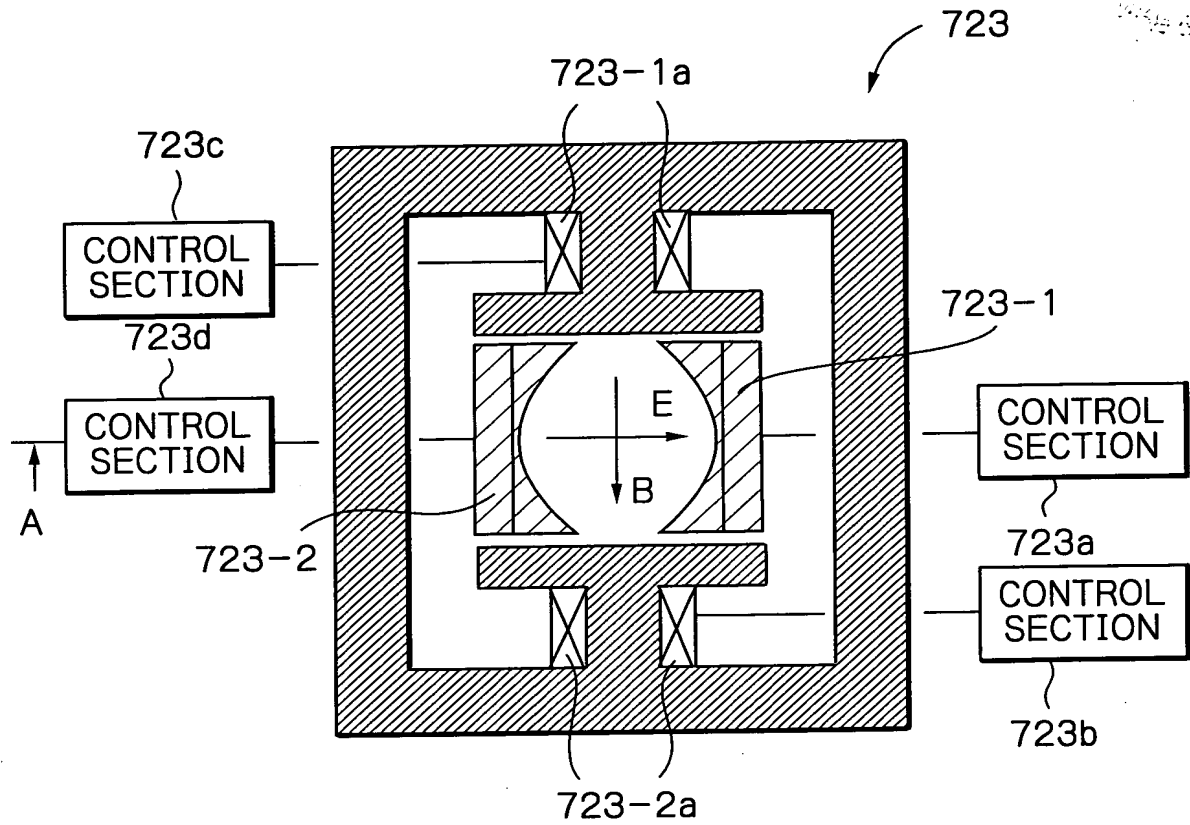


Fig. 10

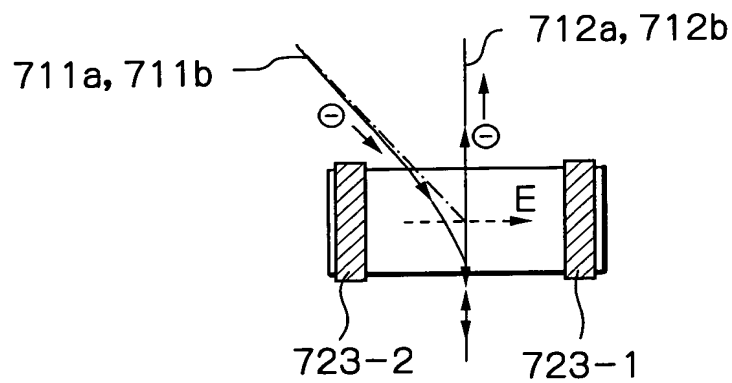


Fig. 11

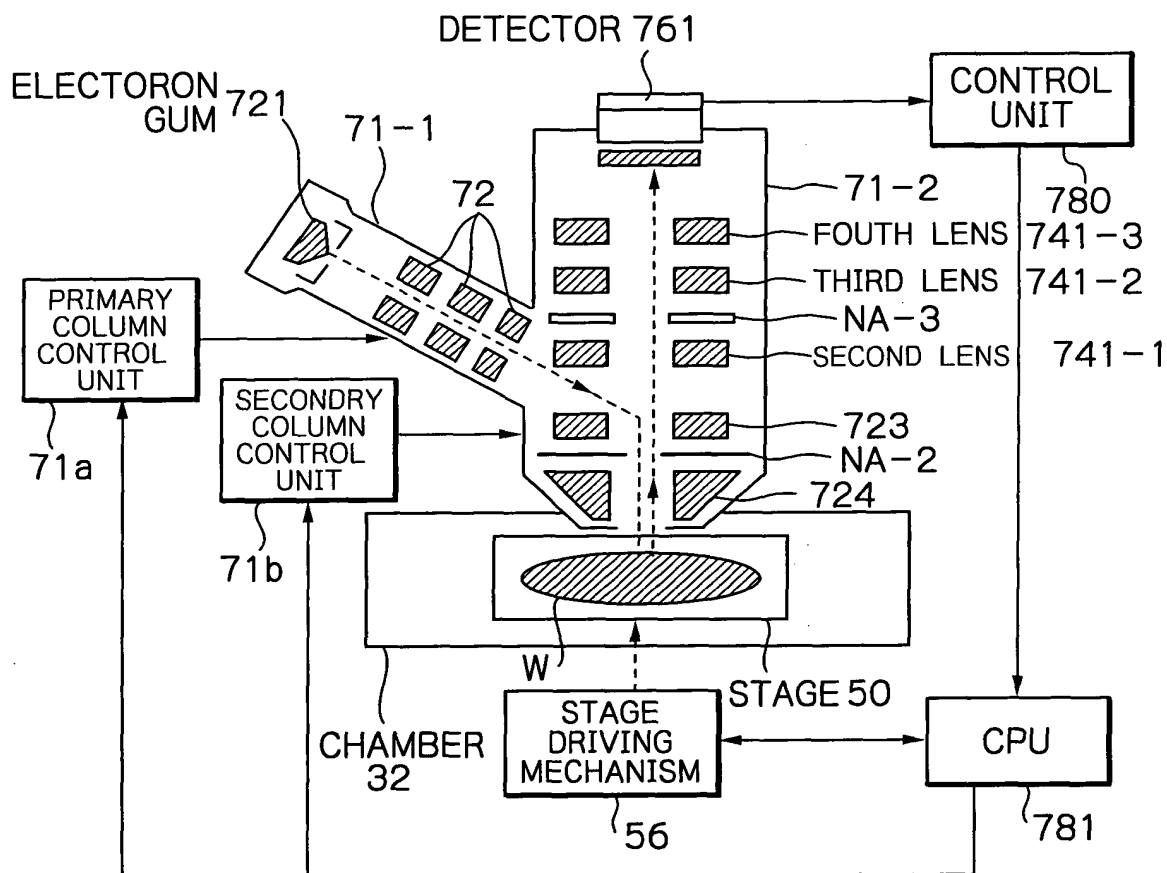




Fig. 12

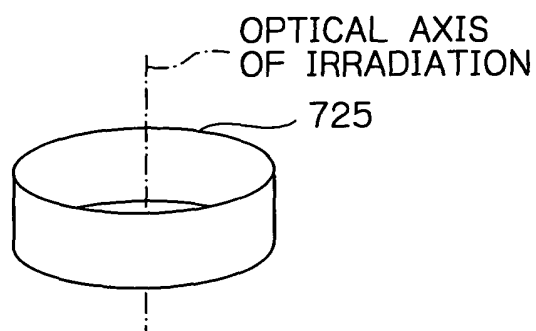
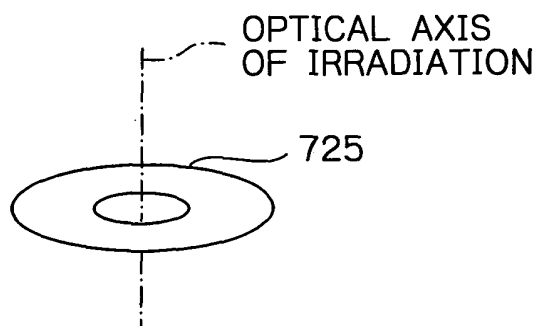


Fig. 13



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F0207-73660

Fig. 14

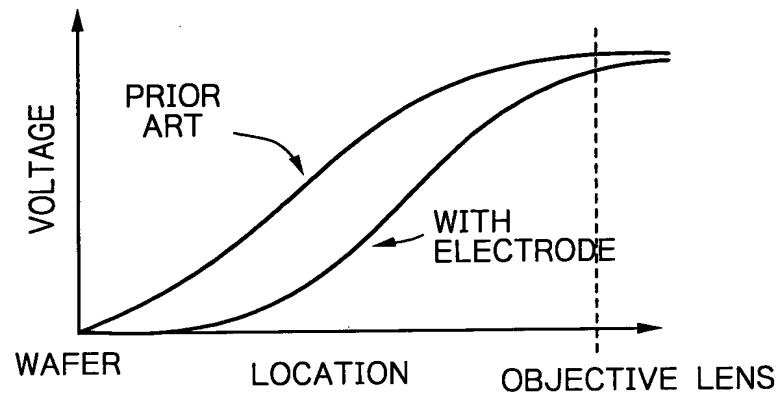
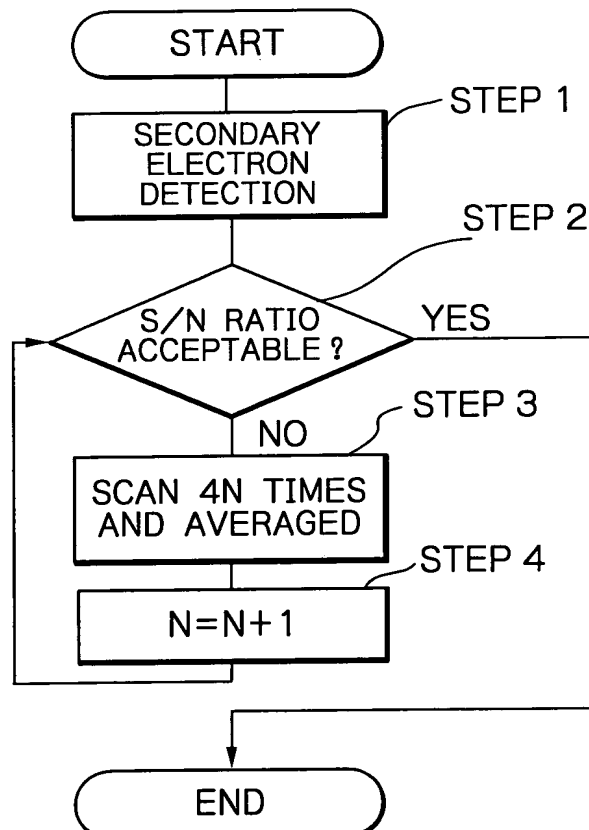
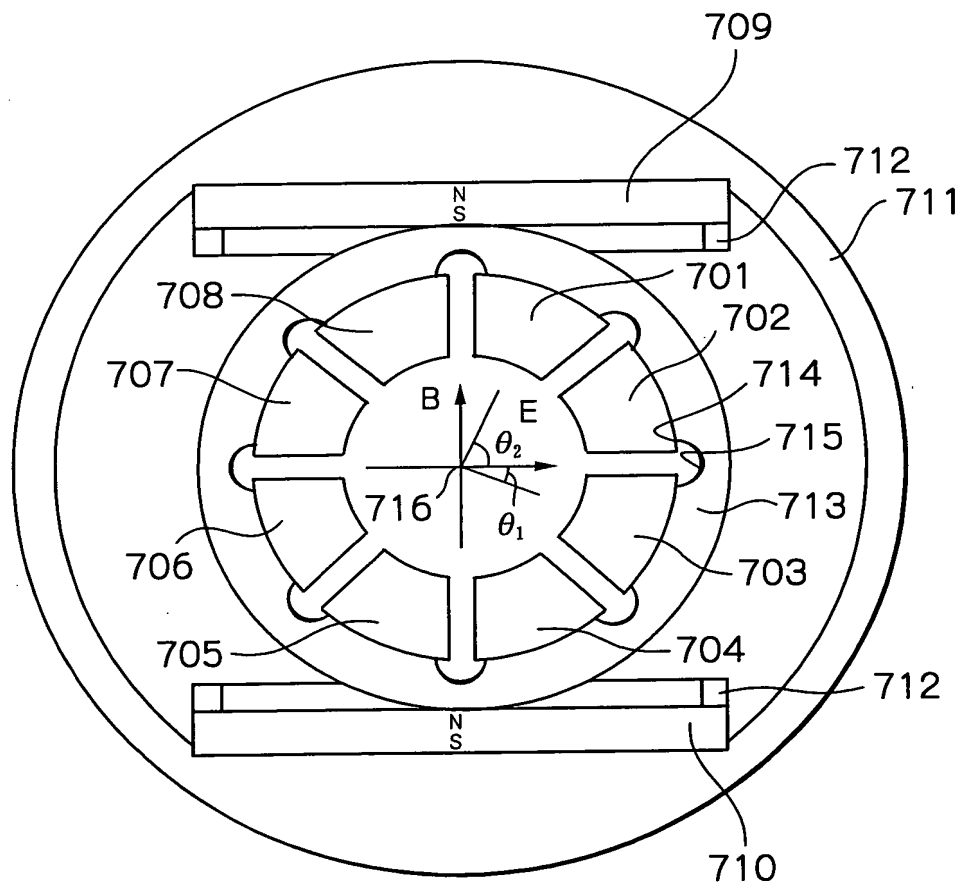


Fig. 15



3910

Fig. 16



1915

A cross-sectional view of a sample chamber 820. The chamber is defined by a thick, hatched wall 826. Inside, a vertical tube 821 is positioned. At the top of the tube is a valve 827. Below the valve, the tube narrows into a tip 824. At the bottom of the tip is a small opening 825. A horizontal line 823 is connected to the side of the tube, leading to a series of three diodes connected in series. The diodes are labeled 820, 823, and 824. The diode 820 is connected to a terminal labeled "TO SAMPLE SUBSTRATE W".

Fig. 20

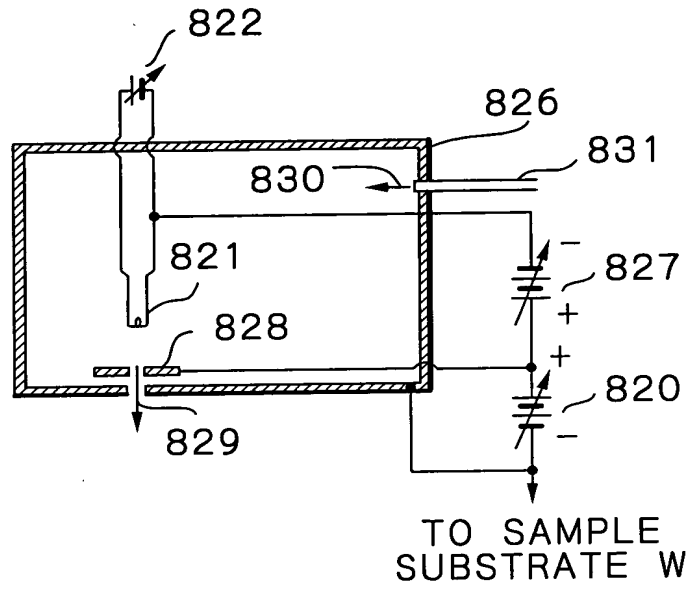
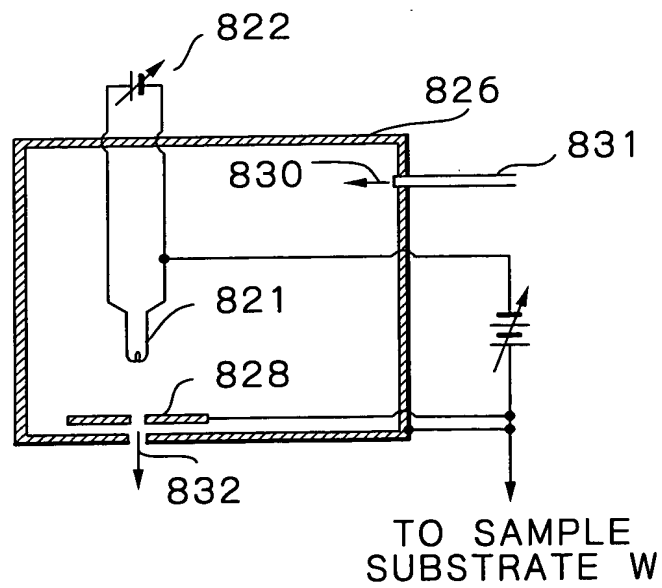
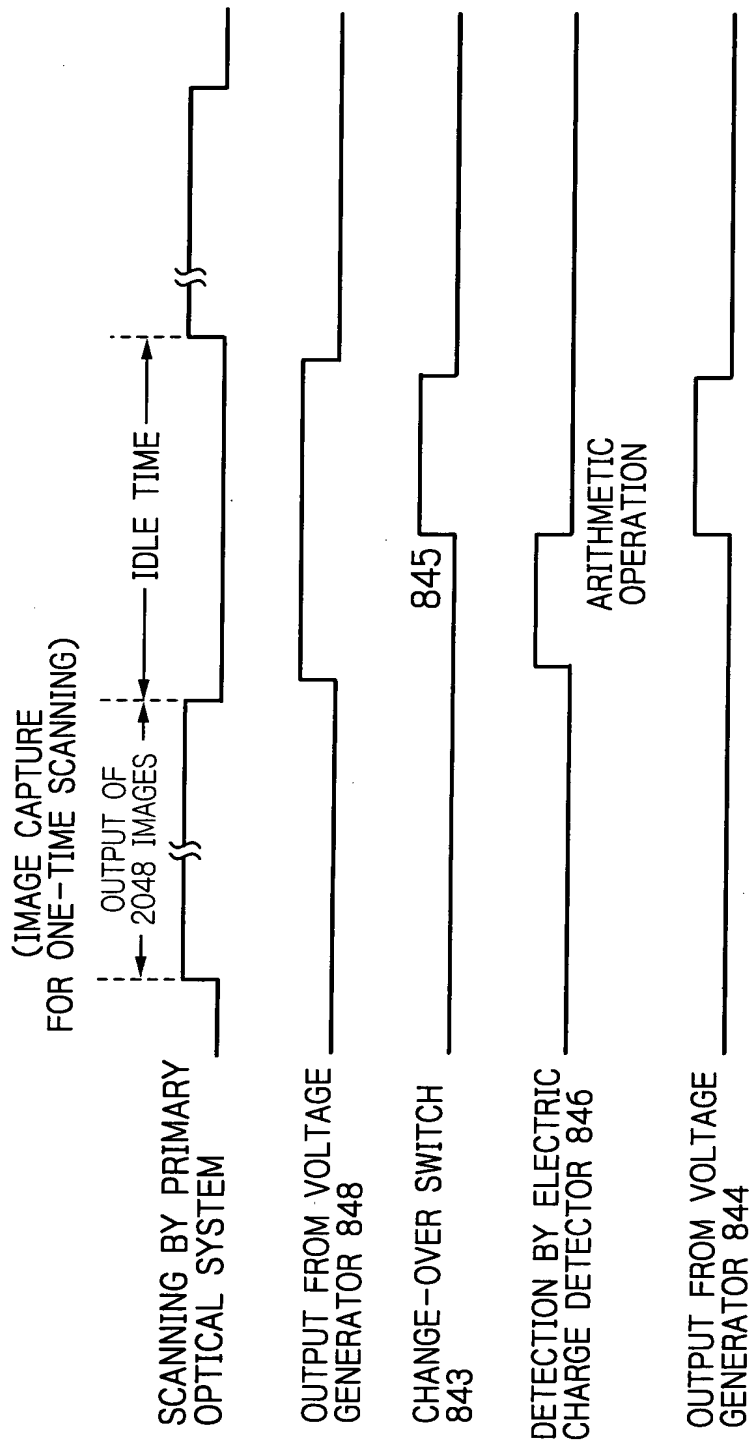


Fig. 21



[illegible]

Fig. 23



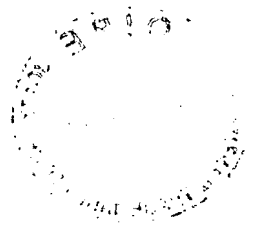


Fig. 24

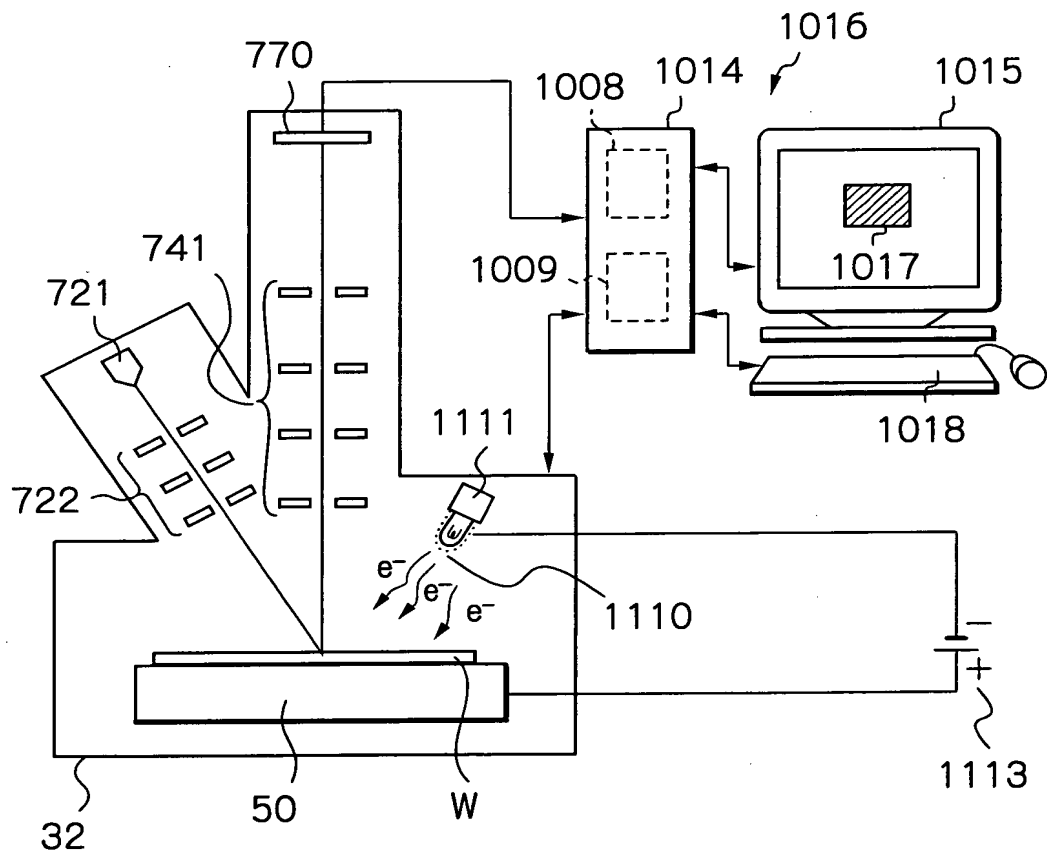


Fig. 25

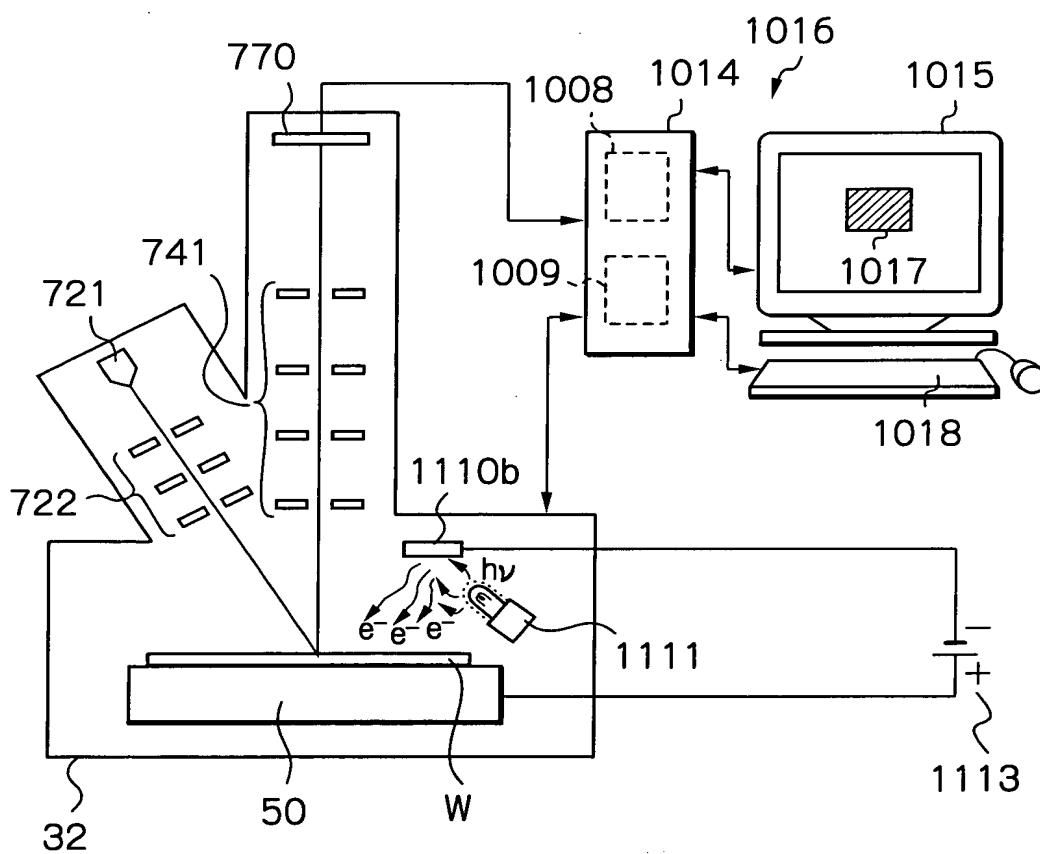


Fig. 28

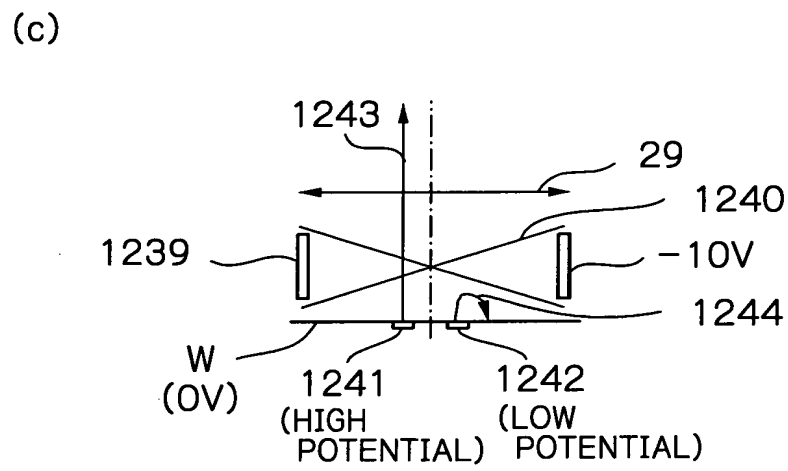
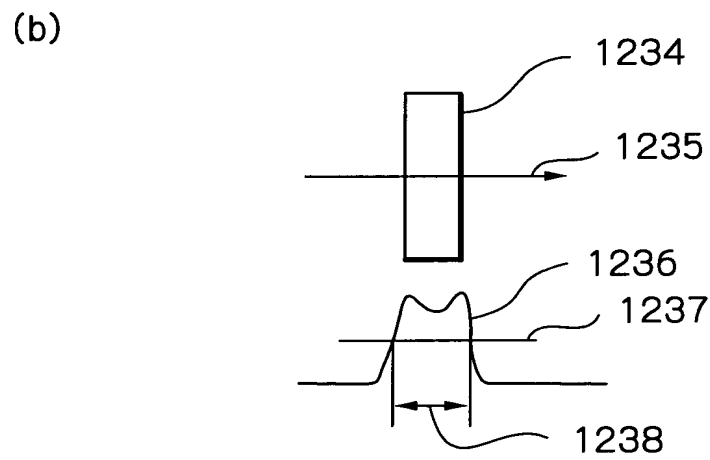
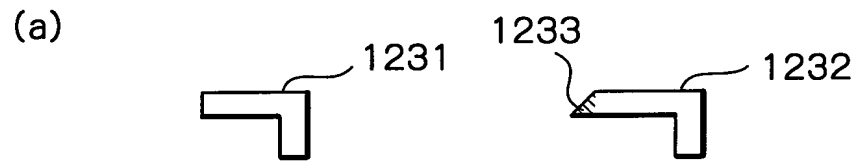




Fig. 29

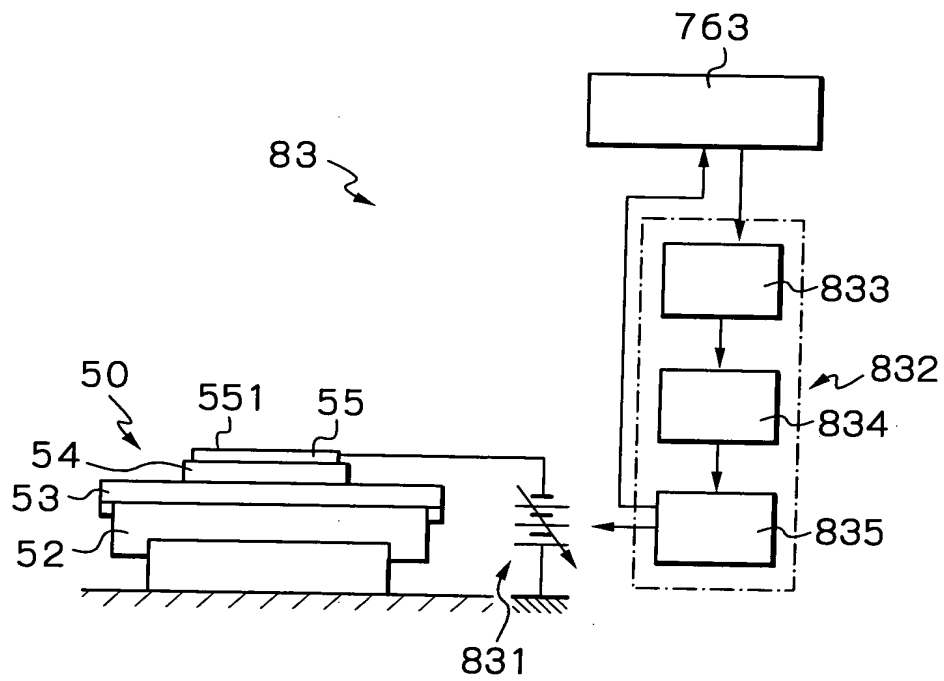


Fig. 30 A.

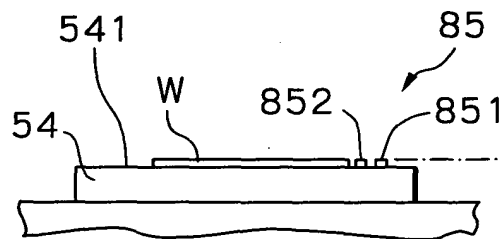


Fig. 30 B

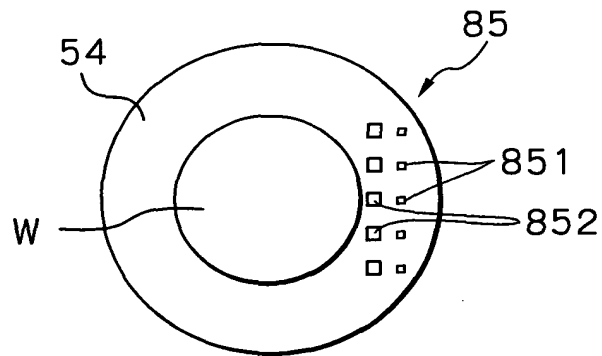




Fig. 31

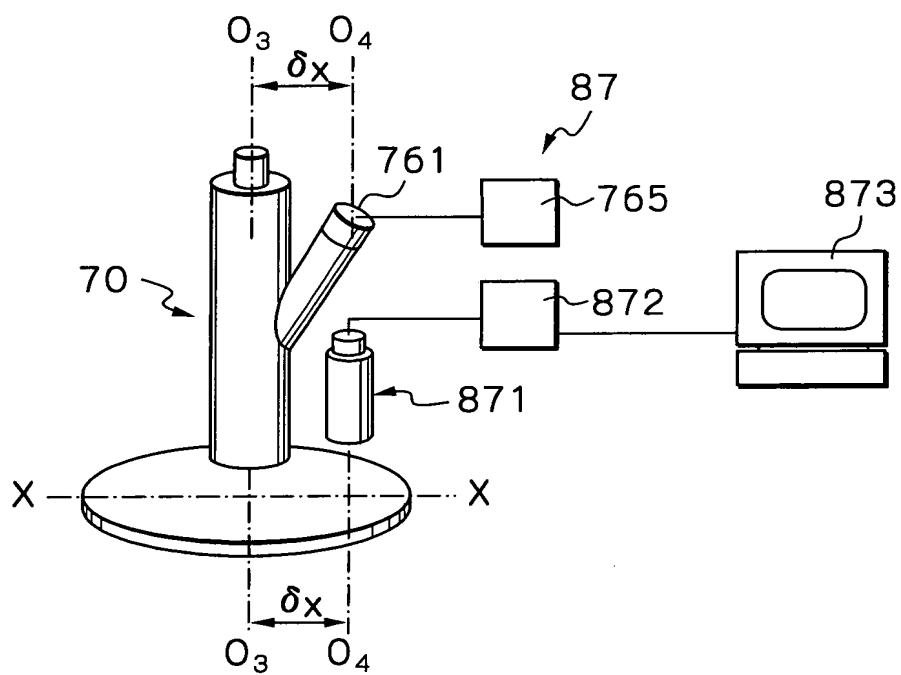
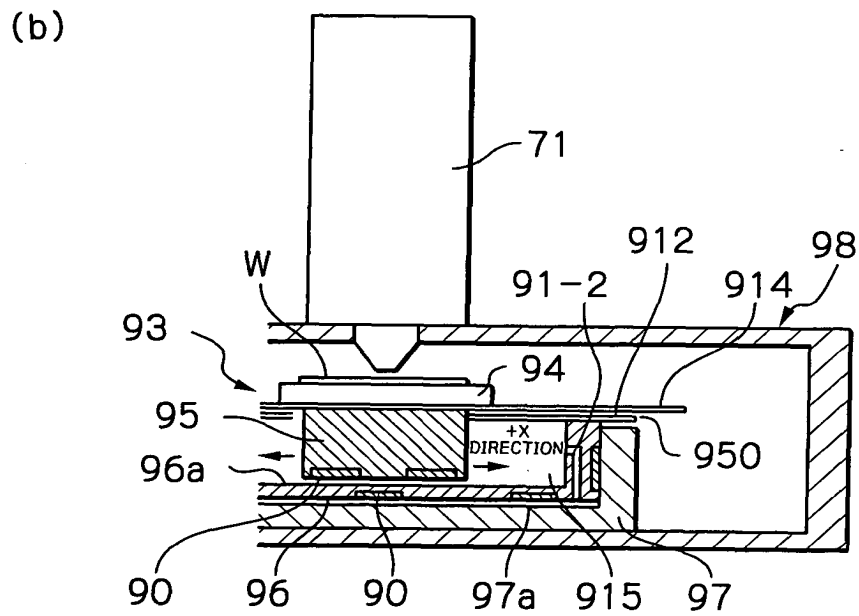
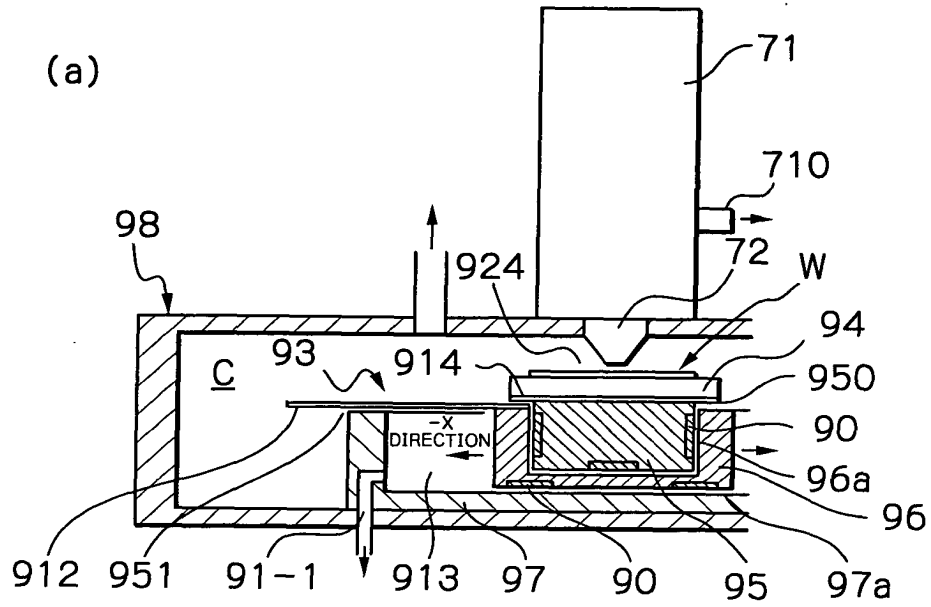
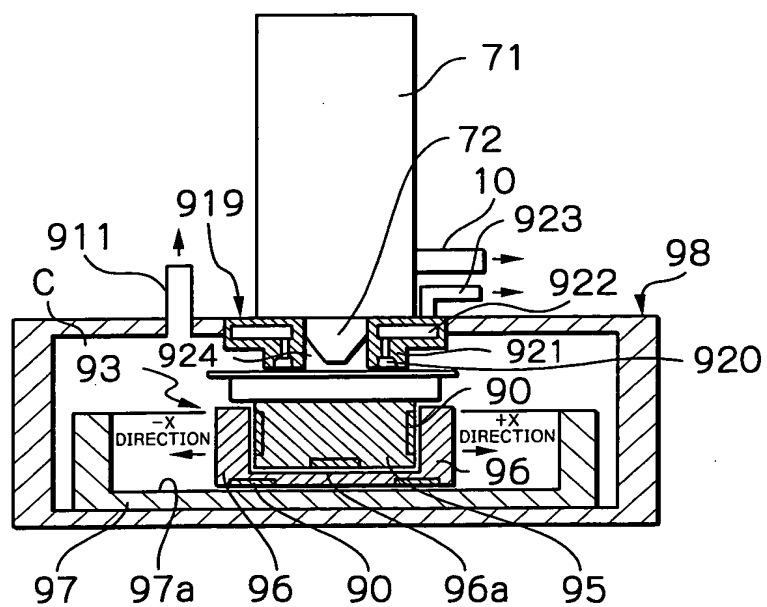
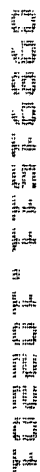


Fig. 32





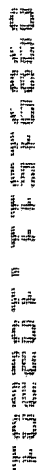
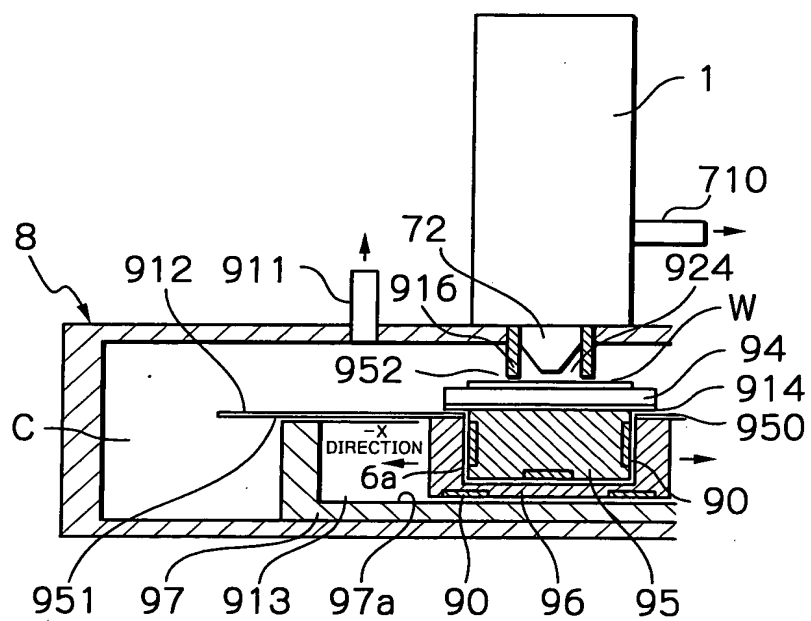


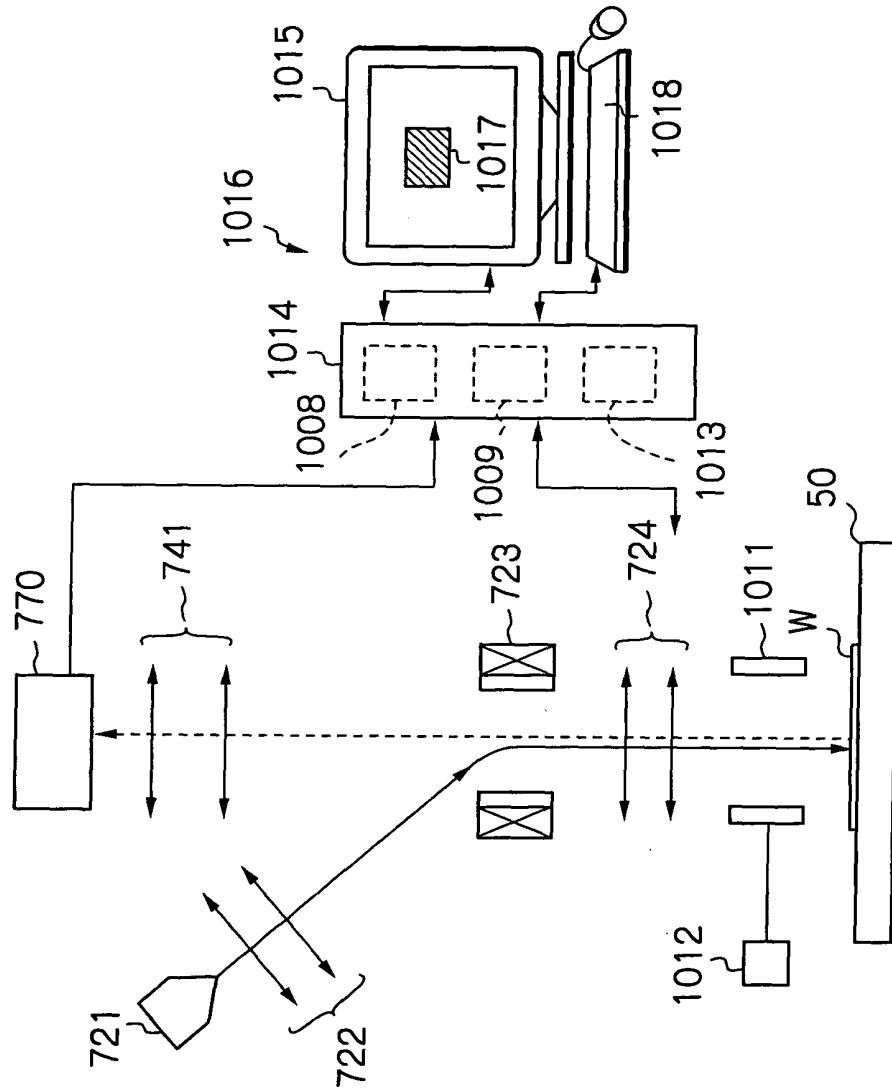
Fig. 36



[illegible]

W

Fig. 41



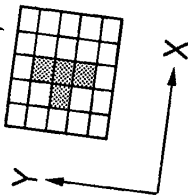
FOOTPRINT 151660

Fig. 42

IMAGES TO BE INSPECTED
AT DIFFERENT LOCATIONS

REFERENCE IMAGE

1036



1032

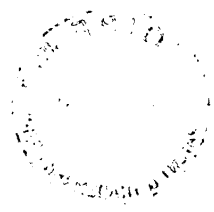
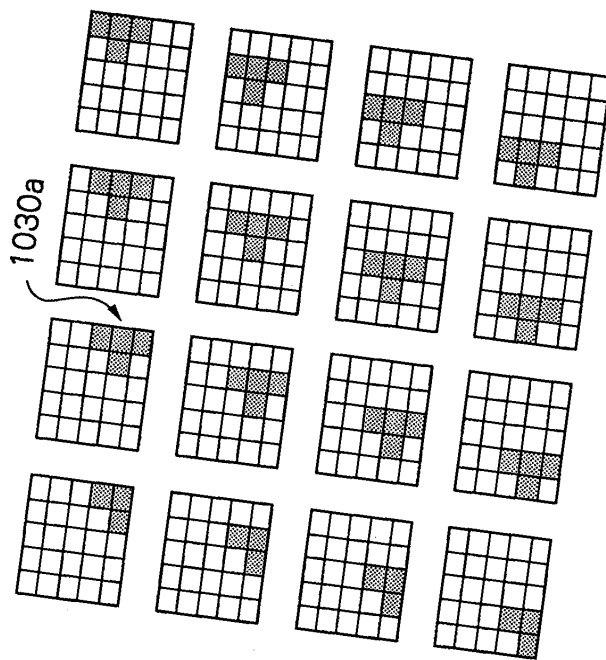
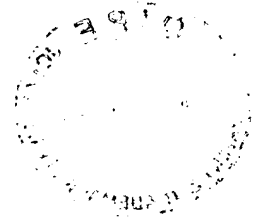
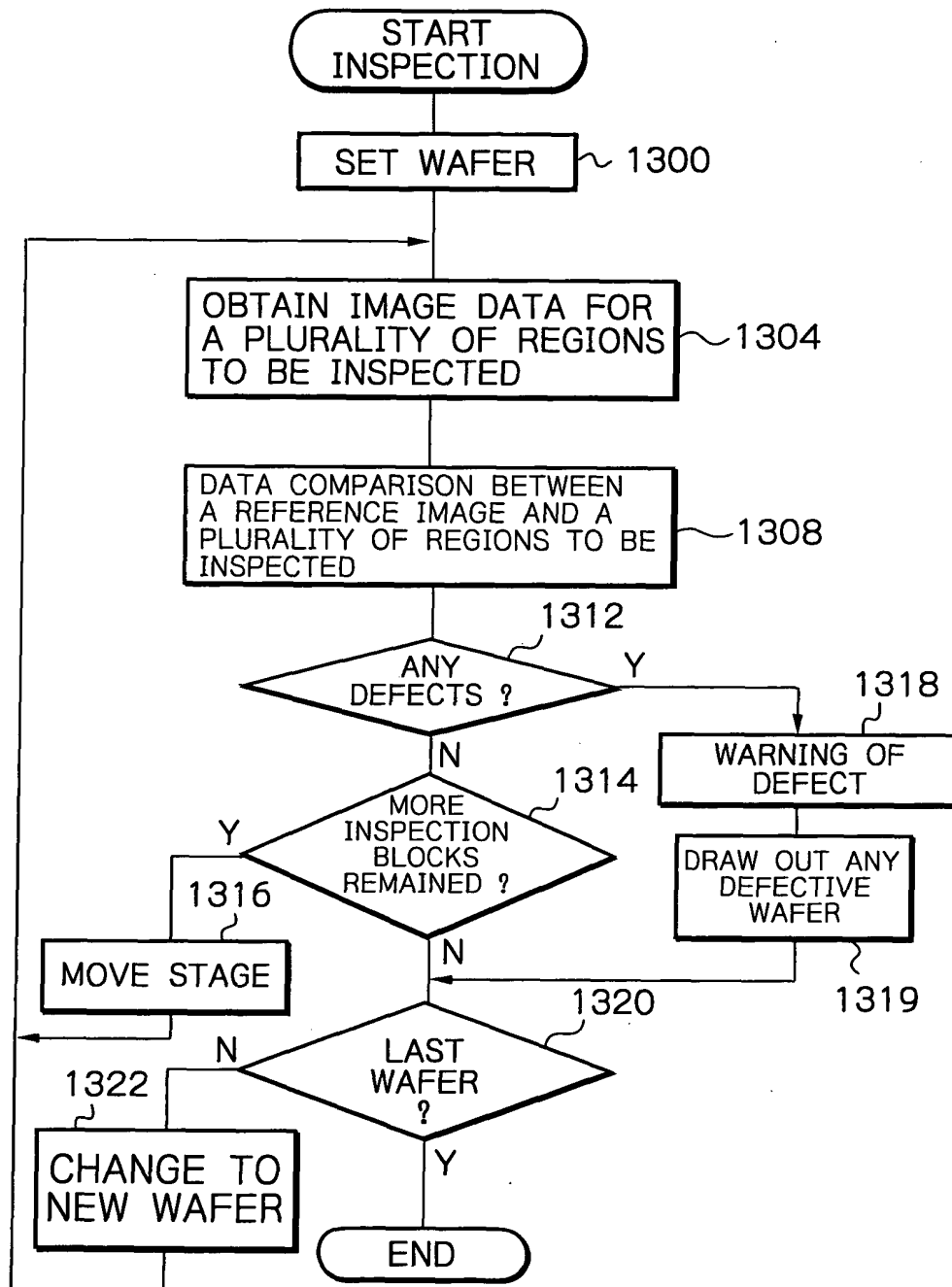


Fig. 43



00041540004

Fig. 44

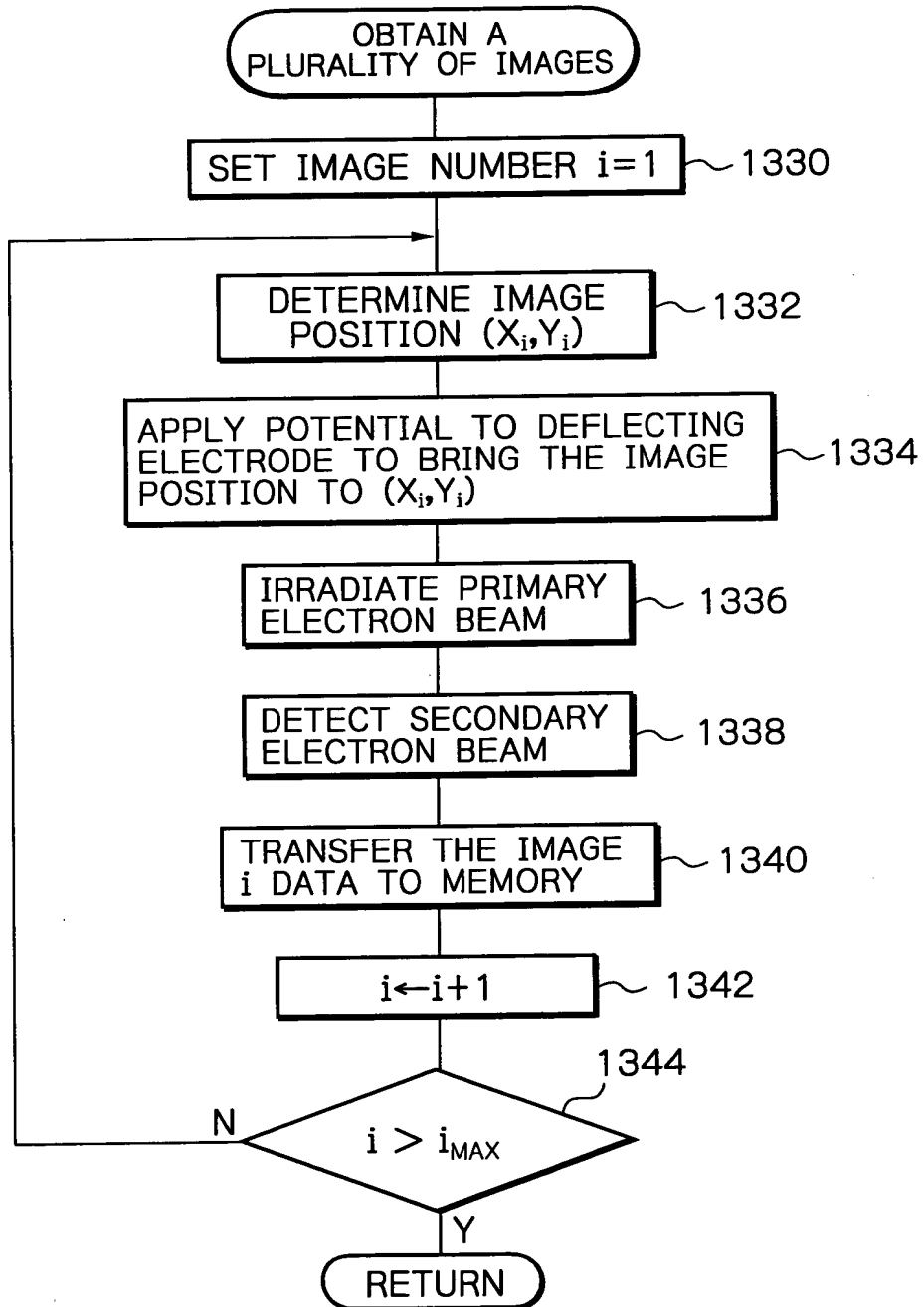


Fig. 45

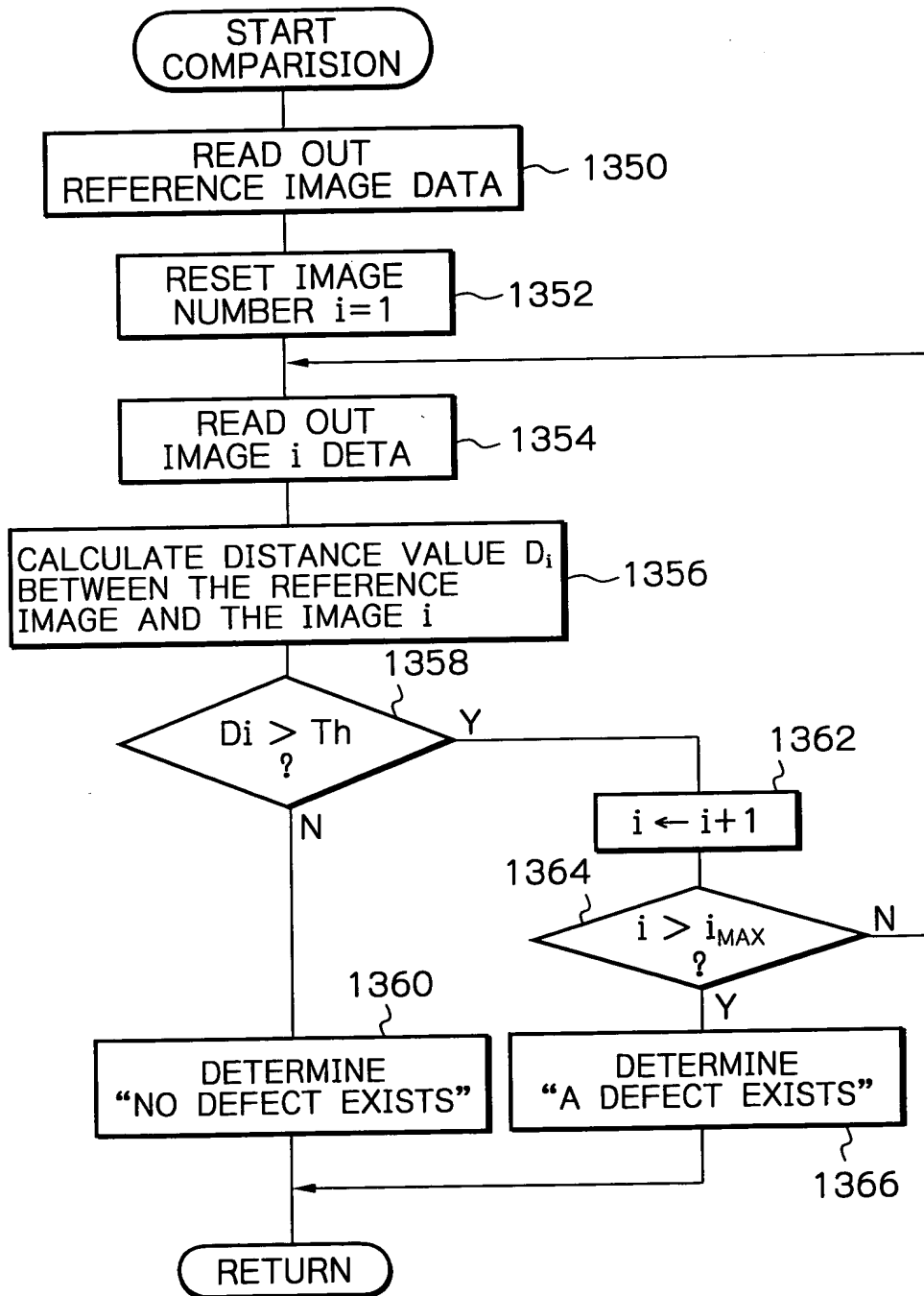
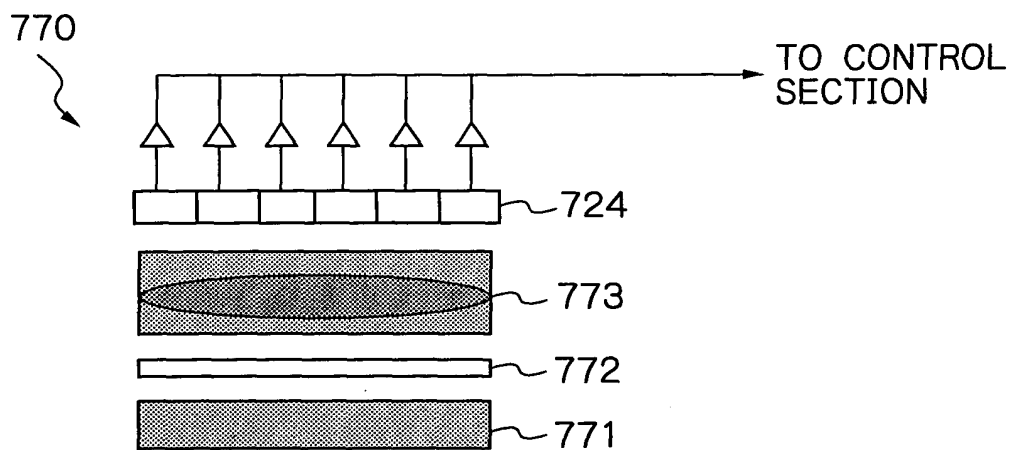




Fig. 46



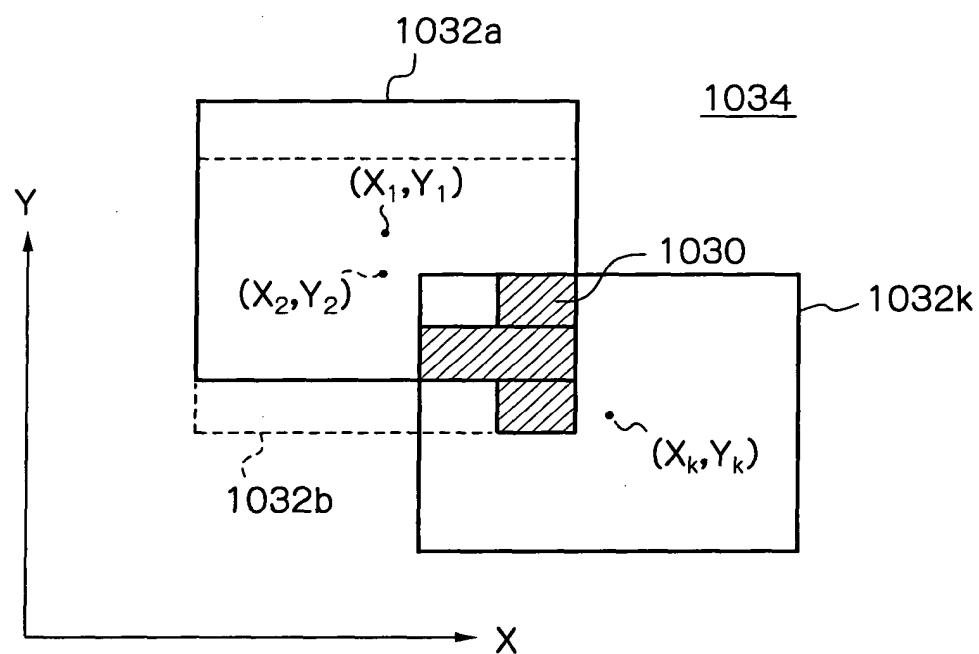
[illegible]

Fig. 48 A

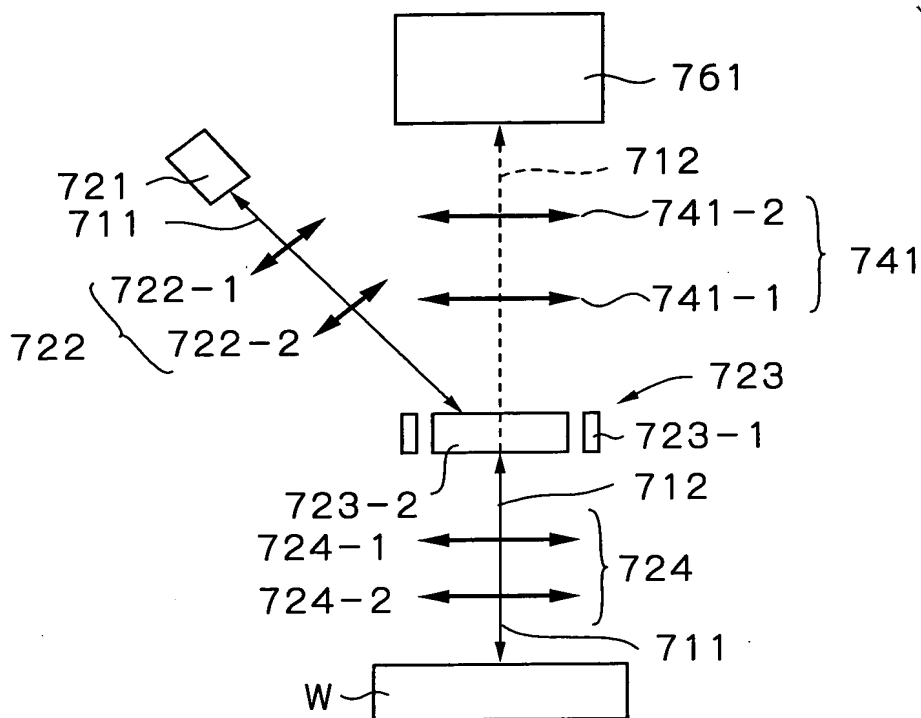


Fig. 48 B

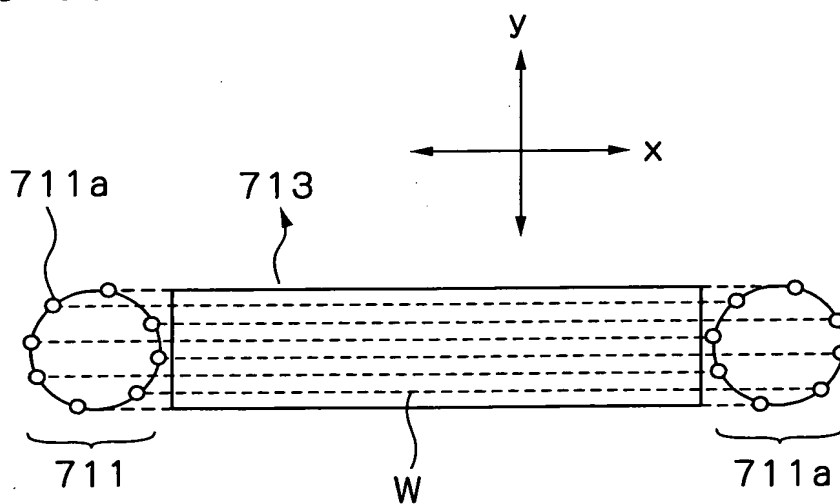


Fig. 49 A

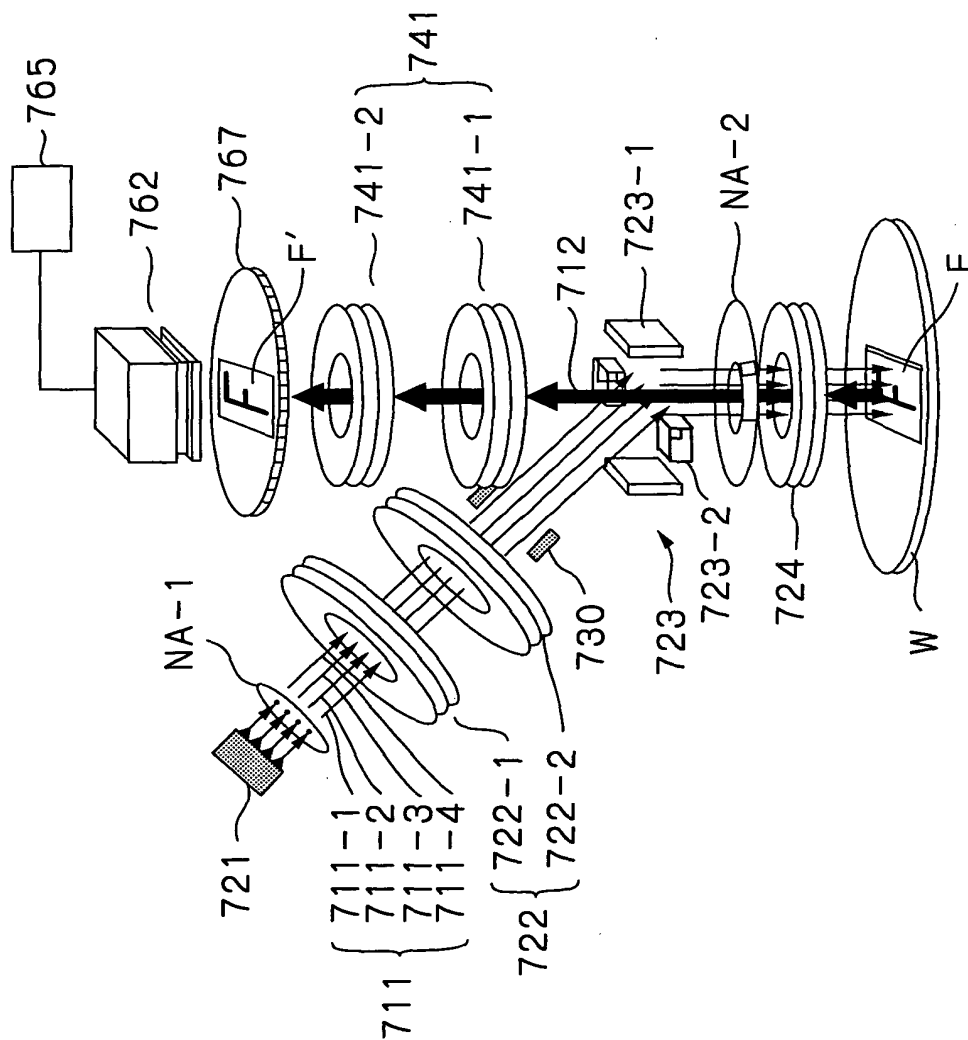


Fig. 49 B

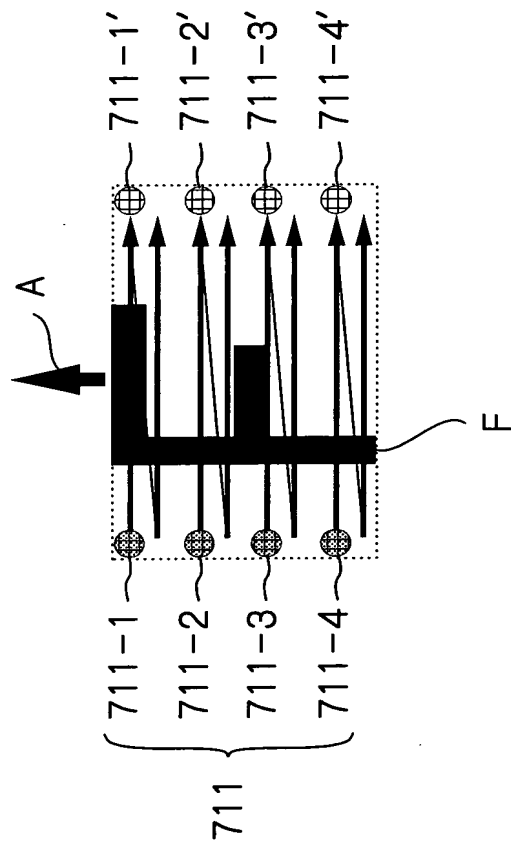
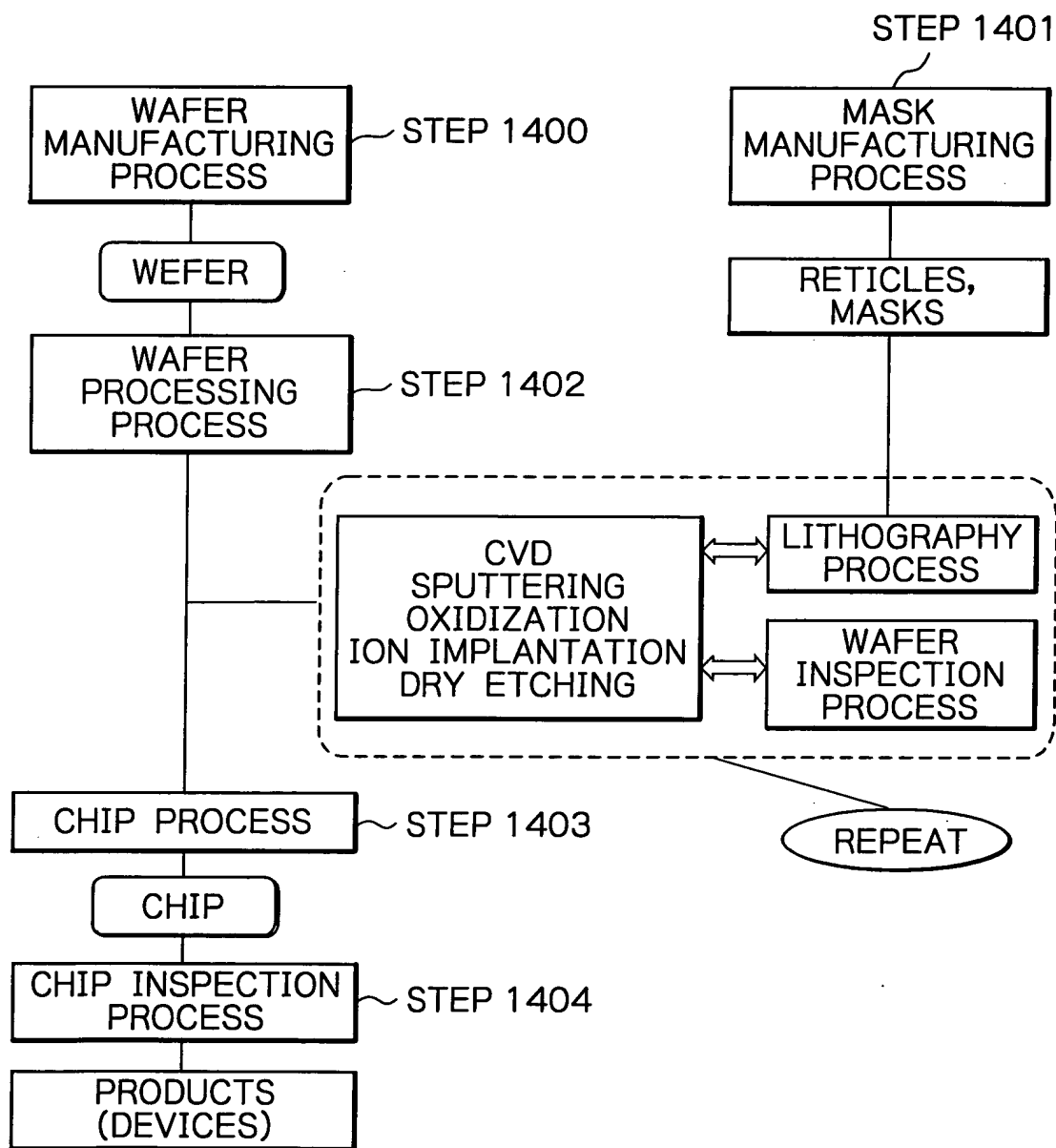


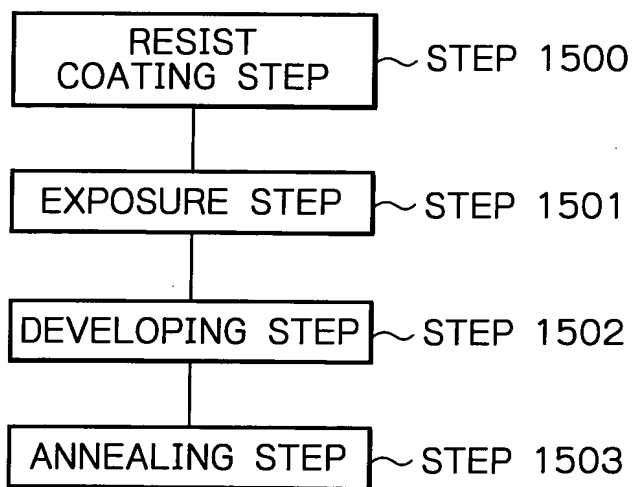
Fig. 50



```

graph TD
    A[RESIST COATING STEP] --> B[EXPOSURE STEP]
    B --> C[DEVELOPING STEP]
    C --> D[ANNEALING STEP]
    A --- A1[STEP 1500]
    B --- B1[STEP 1501]
    C --- C1[STEP 1502]
    D --- D1[STEP 1503]

```



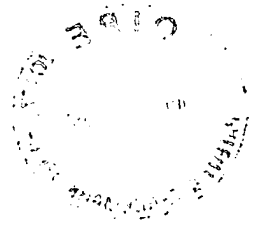
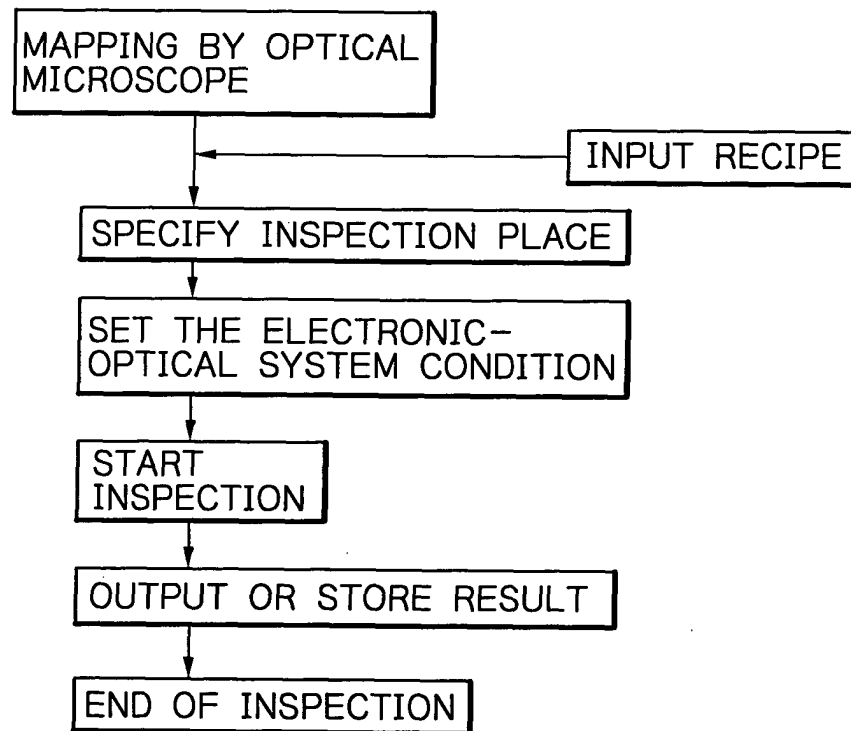


Fig. 51B



0004110001

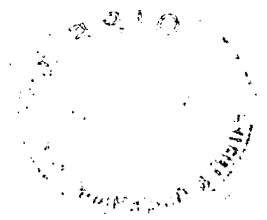
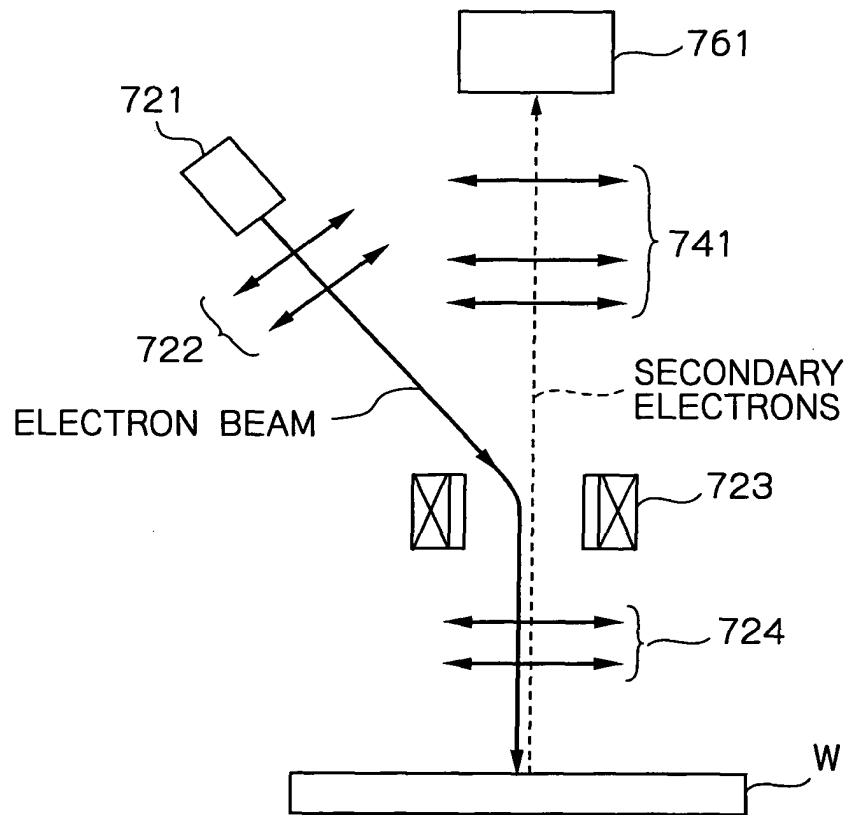
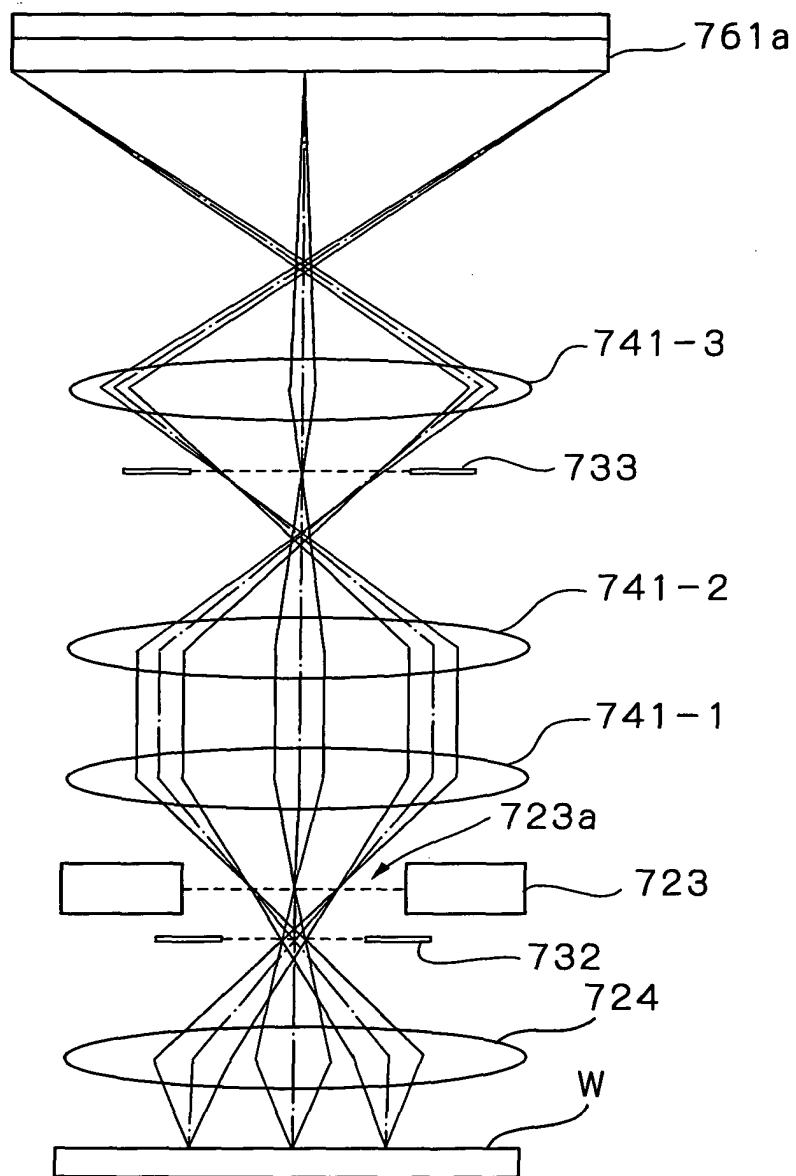
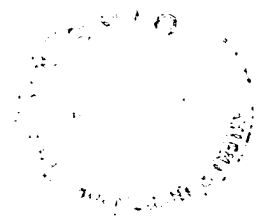


Fig. 52



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F02207 "7576200

Fig. 53



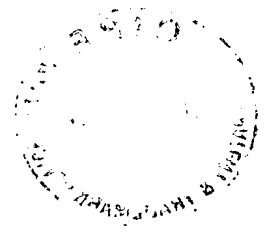


Fig. 54

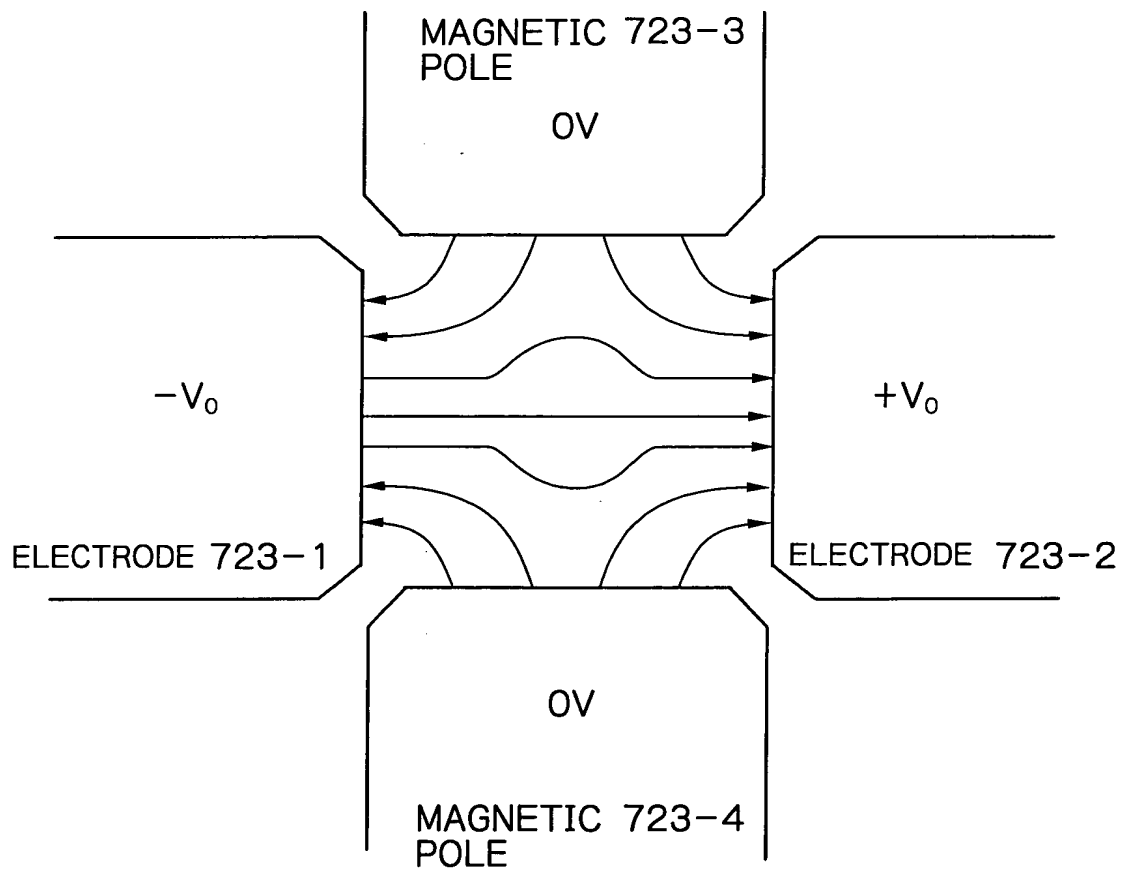
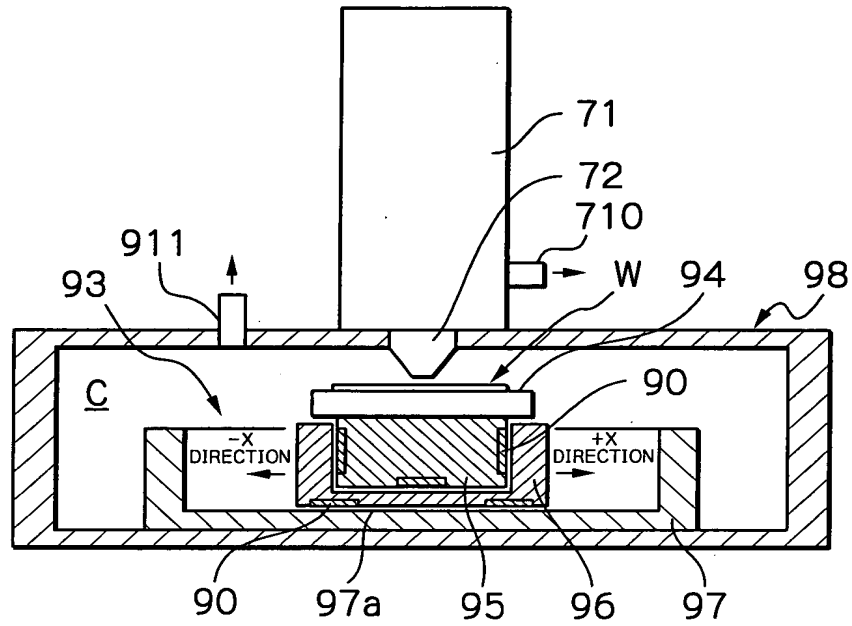


Fig. 55

(a)



(b)

